

**AMENDMENTS TO THE SPECIFICATION**

Please replace paragraph 0005 with the following new paragraph:

[0005] Comparing circuit 307 compares the DC voltage output by level detection circuit 306 with a reference DC voltage, outputting an output signal when the DC voltage output by level detection circuit 306 is higher than the reference voltage ~~that~~ (which signal is inverted relative to the output signal which it outputs produced by comparing circuit 307 when the DC voltage output by level detection circuit 306 is lower than the reference ~~voltage, voltage), for~~ decreasing the gain of variable gain amplifier 305. As a result, an IF signal can be obtained without saturation of the output level of variable gain amplifier 305, even under conditions of forced input of the signal received from antenna 301.

Please replace paragraph 0047 with the following new paragraph:

[0047] Comparing circuit 7 compares the DC voltage output by level detection circuit 6 and a reference DC voltage, outputting an output signal when the DC voltage output by level detection circuit 6 is higher than the reference voltage ~~that~~ (which signal is inverted relative to the output signal which it outputs produced by comparing circuit 307 when the DC voltage output by level detection circuit 6 is lower than the reference ~~voltage, voltage), for~~ decreasing the gain of variable gain amplifier 5. Simultaneous therewith, output from comparing circuit 7 is input at counter circuit 16.